



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of:

Christine Hau-Riege

Group Art Unit: 2823

Application No.: 10/697,214

Examiner: William M. Brewster

Filed: October 29, 2003

Title: SEMICONDUCTOR COMPONENT AND METHOD FOR PRECLUDING STRESS-INDUCED VOID FORMATION IN THE SEMICONDUCTOR COMPONENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that the following attached item(s):

1. Check for \$1403.00;
2. Part B - Issue Fee Transmittal form;
3. Amendment Under 37 C.F.R. § 1.312;
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I hereby certify that this document (and any as referred to as being attached or enclosed) is being deposited with sufficient postage as first class mail with the United States Postal Service on May 5, 2005 and addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

Dated: 5 MAY 2005

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